

## Refine Search

### Search Results -

Terms	Documents
L1.clm. and (PCI or "PCI Express").clm.	15

**Database:**

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**Search:**

L3

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**Hit Count****Set Name**

result set

<u>L3</u>	L1.clm. and (PCI or "PCI Express").clm.	15	<u>L3</u>
<u>L2</u>	L1 and (PCI or "PCI Express")	5401	<u>L2</u>
<u>L1</u>	(port near10 connect\$4) same (mode or type or phase)	23877	<u>L1</u>

END OF SEARCH HISTORY

## Refine Search

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### Search Results -

Terms	Documents
L6 and (board or card or motherboard)	695

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**Search:** L7

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 result set

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*DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR*

L7	L6 and (board or card or motherboard)	695	L7
L6	L4 same (PCI or "PCI Express")	805	L6
L5	L4 and (PCI or "PCI Express")	7854	L5
L4	(port near10 connect\$4) same (mode or type or phase)	104454	L4

*DB=PGPB; PLUR=YES; OP=OR*

L3	L1.clm. and (PCI or "PCI Express").clm.	15	L3
L2	L1 and (PCI or "PCI Express")	5401	L2
L1	(port near10 connect\$4) same (mode or type or phase)	23877	L1

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### Search Results -

Terms	Documents
(375/376  361/679  361/683  361/783  345/520  345/531  370/254  709/253  710/100  710/33  710/300  710/301  710/302  710/72  710/306  710/313  326/37).ccls.	16879

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**Search:** L8

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<u>Name Query</u>	<u>Na</u>	<u>res</u>
side by side		
DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR		
L8 710/100,33,300-	16879	L
L8 302,72,306,313;345/520,531;361/679,683,783;709/253;326/37;375/376;370/254.ccls.	16879	L
L7 L6 and (board or card or motherboard)	695	L
L6 L4 same (PCI or "PCI Express")	805	L
L5 L4 and (PCI or "PCI Express")	7854	L
L4 (port near10 connect\$4) same (mode or type or phase)	104454	L
DB=PGPB; PLUR=YES; OP=OR		
L3 L1.clm. and (PCI or "PCI Express").clm.	15	L
L2 L1 and (PCI or "PCI Express")	5401	L
L1 (port near10 connect\$4) same (mode or type or phase)	23877	L

END OF SEARCH HISTORY

## Refine Search

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### Search Results -

Terms	Documents
L7 and L8	70

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**Search:** L10

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*DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR*

<u>L10</u>	l7 and L8	70	<u>L</u>
<u>L9</u>	l6 and L8	78	<u>L</u>
<u>L8</u>	710/100,33,300- 302,72,306,313;345/520,531;361/679,683,783;709/253,326/37,375/376;370/254.ccls.	16879	<u>L</u>
<u>L7</u>	L6 and (board or card or motherboard)	695	<u>L</u>
<u>L6</u>	L4 same (PCI or "PCI Express")	805	<u>L</u>
<u>L5</u>	L4 and (PCI or "PCI Express")	7854	<u>L</u>
<u>L4</u>	(port near10 connect\$4) same (mode or type or phase)	104454	<u>L</u>
<i>DB=PGPB; PLUR=YES; OP=OR</i>			
<u>L3</u>	L1.clm. and (PCI or "PCI Express").clm.	15	<u>L</u>
<u>L2</u>	L1 and (PCI or "PCI Express")	5401	<u>L</u>
<u>L1</u>	(port near10 connect\$4) same (mode or type or phase)	23877	<u>L</u>

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	11	tranceivers same enable	US-PGPUB; USPAT; EPO; JPO	ADJ	OFF	2007/04/02 09:15
L2	26509	driver same receiver	US-PGPUB; USPAT; EPO; JPO	ADJ	OFF	2007/04/02 09:15
L3	1857	enable same l2	US-PGPUB; USPAT; EPO; JPO	ADJ	OFF	2007/04/02 09:20
L4	15017	enable\$4 near3 driver	US-PGPUB; USPAT; EPO; JPO	ADJ	OFF	2007/04/02 09:20
L5	12073	enable\$4 near3 receiver	US-PGPUB; USPAT; EPO; JPO	ADJ	OFF	2007/04/02 09:22
L6	3866	disable\$4 near3 driver	US-PGPUB; USPAT; EPO; JPO	ADJ	OFF	2007/04/02 09:23
L7	2221	disable\$4 near3 receiver	US-PGPUB; USPAT; EPO; JPO	ADJ	OFF	2007/04/02 09:23
L8	32	l4 same l5 same l6 same l7	US-PGPUB; USPAT; EPO; JPO	ADJ	OFF	2007/04/02 09:23



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## Introduction to PCI Express-a new high speed serial data bus

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Dept. of Phys., Yale Univ., New Haven, CT, USA

This paper appears in: [Nuclear Science Symposium Conference Record, 2005 IEEE](#)

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Number of Pages: CD-ROM

ISSN: 1082-3654

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Digital Object Identifier: 10.1109/NSSMIC.2005.1596352

Posted online: 2006-02-27 09:49:04.0

### Abstract

Most new PCs have a new bus called PCI Express. It is a very high speed dual-simplex, point to point serial differential low voltage interconnect. The signaling rate is 2.5 Gbit per second, with 8/10 bit encoding to embed the clock in the data stream. On the transmit side parallel data is shifted out serially and on the receive side serial data is shifted into registers for parallel data output. The receiver also recovers the embedded clock. This new bus can be used to connect modules or boxes via twisted pair copper and is the bus of choice for high speed applications in the industrial world.

### Index Terms

Inspec

[Controlled Indexing](#) [embedded systems](#) [microcomputers](#) [peripheral interfaces](#) [system buses](#)

[Non-controlled Indexing](#)

[2.5 Gbit/s PCI Express](#) [data stream](#) [embedded clock](#) [high speed serial data bus](#) [parallel data](#) [parallel data output](#) [point-to-point serial different](#) [low voltage interconnect](#) [receive side](#) [registers](#) [serial data](#) [signaling rate](#) [transmit side](#) [twisted pair copper](#) [very high speed dual-simplex](#)

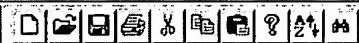
**Author Keywords**

Not Available

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File View Edit Tools Window Help



- Drafts
- Pending
- Active
  - L1: (9727) (port near10 conn
  - L2: (97) 11 same (PCI or "PC
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DBs USPAT

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Plurals

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BRS form IS&R form Image Text HTML

Type	L #	Hits	Search Text	DBs	Time Stamp	Comments	Error Definition	Err
1	BRS	L1	9727 (port near10 connect\$4) same mode	USPAT	2007/04/02 10:58			
2	BRS	L2	97 11 same (PCI or "PCI Express")	USPAT	2007/04/02 10:59			
3	BRS	L3	32 12 same (board or card or motherboard)	USPAT	2007/04/02 10:59			

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  - L3: (32) 12 same (board or c
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DBs USPAT

Plurals

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Default operator: OR

12 same (board or card or motherboard)

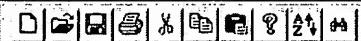
BRS form IS&R form Image Text HTML

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1	<input type="checkbox"/>	<input type="checkbox"/>	US 7185135 B1	20070227	16	USB to PCI bridge	710/315	710/313
2	<input type="checkbox"/>	<input type="checkbox"/>	US 7161926 B2	20070109	21	Low-latency multi-hop ad hoc wireless network	370/338	455/434
3	<input type="checkbox"/>	<input type="checkbox"/>	US 7150397 B2	20061219	10	Dual mode controller for ISO7816 and USB enabled	235/451	235/487; 235/492
4	<input type="checkbox"/>	<input type="checkbox"/>	US 7117013 B2	20061003	22	Cutable dual mode ISM and U-NII wireless radio with	455/558	455/552.1; 455/557
5	<input type="checkbox"/>	<input type="checkbox"/>	US 7000052 B2	20060214	9	System and method for configuring and deploying	710/300	710/104; 713/1;
6	<input type="checkbox"/>	<input type="checkbox"/>	US 6977655 B2	20051220	11	Dual mode DDR SDRAM/SGRAM	345/533	345/534; 345/545.

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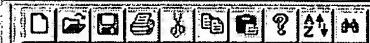
BRS form IS&R form Image Text HTML

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7	<input type="checkbox"/>	<input type="checkbox"/>	US 6913196 B2	20050705	9	Dual mode controller for ISO7816 and USB enabled	235/451	235/380; 235/487;
8	<input type="checkbox"/>	<input type="checkbox"/>	US 6873261 B2	20050329	22	Early warning near-real-time security system	340/574	340/426.1; 340/539.18;
9	<input type="checkbox"/>	<input type="checkbox"/>	US 6807167 B1	20041019	6	Line card for supporting circuit and packet switching	370/352	370/357; 370/389;
10	<input type="checkbox"/>	<input type="checkbox"/>	US 6715094 B2	20040330	26	Mult-mode I/O interface for synchronizing selected	713/400	710/105; 710/58;
11	<input type="checkbox"/>	<input type="checkbox"/>	US 6621496 B1	20030916	11	Dual mode DDR SDRAM/SGRAM	345/533	345/534; 345/545;
12	<input type="checkbox"/>	<input type="checkbox"/>	US 6597660 B1	20030722	24	Method for real-time traffic analysis on packet networks	370/230.1	370/236

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10	<input type="checkbox"/>	<input type="checkbox"/>	US 6715094 B2	20040330	26	Mult-mode I/O interface for synchronizing selected	713/400	710/105; 710/58;
11	<input type="checkbox"/>	<input type="checkbox"/>	US 6621496 B1	20030916	11	Dual mode DDR SDRAM/SGRAM	345/533	345/534; 345/545;
12	<input type="checkbox"/>	<input type="checkbox"/>	US 6597660 B1	20030722	24	Method for real-time traffic analysis on packet networks	370/230.1	370/236
13	<input type="checkbox"/>	<input type="checkbox"/>	US 6559769 B2	20030506	21	Early warning real-time security system	340/574	340/426.1; 340/506;
14	<input type="checkbox"/>	<input type="checkbox"/>	US 6549981 B2	20030415	23	Disk array system with controllers that automate	711/114	711/112
15	<input type="checkbox"/>	<input type="checkbox"/>	US 6525981 B2	20030225	13	Full page increment/decrement burst	365/223	365/230.06; 365/236

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1. **Introduction to PCI Express-a new high speed serial data bus**  
 Dhawan, S.K.;  
Nuclear Science Symposium Conference Record, 2005 IEEE  
 Volume 2, 23-29 Oct. 2005 Page(s):687 - 691  
 Digital Object Identifier 10.1109/NSSMIC.2005.1596352

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2. **Building cost effective high performance computing environment via PCI**  
 Nishida, A.;  
Parallel Processing Workshops, 2006. ICPP 2006 Workshops. 2006 International  
 14-18 Aug. 2006 Page(s):8 pp.

Digital Object Identifier 10.1109/ICPPW.2006.27  
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3. **Auto-referenced on-die power supply noise measurement circuit**  
 Chansungsan, C.;  
Custom Integrated Circuits Conference, 2005. Proceedings of the IEEE 2005  
 18-21 Sept. 2005 Page(s):39 - 42

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